



# 3<sup>rd</sup> International Symposium on Devices, Circuits and Systems



Organized by  
**Indian Institute of Engineering Science and Technology, Shibpur (Kolkata), India**  
**In Collaboration with Hiroshima University, Japan**

(<http://www.isdcs.iiests.ac.in>)

**March 04-06, 2020**

#### Chief Patron

V.K Aatre, Chairman, BOG, IEST Shibpur

#### Patron

P. Chakrabarti, Director, IEST Shibpur  
M. Ochi, President, Hiroshima University

#### General Chairs

Hafizur Rahaman, IEST Shibpur  
Hans Juergen Mattausch, Hiroshima University

#### Program Chairs

Chandan Giri, IEST Shibpur  
Partha Bhattacharyya, IEST Shibpur  
D. Navarro, Hiroshima University, Japan

#### Organizing Chairs

Surajit Roy, IEST Shibpur  
Malay Kule, IEST Shibpur

#### Workshop Chairs

Mainak Sengupta, IEST Shibpur  
Partha Sarathi Gupta, IEST Shibpur

#### Workshop Secretary

Nillohit Mukherjee, IEST Shibpur  
Tamal Ghosh, IEST Shibpur

#### Publicity Chairs

Pranab Roy, IEST Shibpur  
Sandip Bhattacharya, Hiroshima University, Japan

#### Publication Chairs

Prasun Ghosal, IEST Shibpur  
Surajeet Ghosh, IEST Shibpur

#### Registration Chairs

Indrajit Banerjee, IEST Shibpur  
Tamaghna Acharya, IEST Shibpur

ISDCS 2020 focuses on the recent innovation of Devices, Circuits and Systems to highlight the strong foundation in methodology and the integration of multidisciplinary approaches. The scientists, researchers and engineers are globally innovating to change the way in which devices and circuits are understood, optimized, and leveraged in a variety of systems and applications. The theme for the conference this year is "*Micro- & Nano- Electronic Devices and Systems (MNEDS)*". This three-day conference comprises of Pre-Conference Workshop on first day (March 4, 2020), followed by the main conference during the last two days (March 05-06, 2020). Workshop focuses on the detailed discussion on the recent development in the area of Devices, Circuits and Systems.

Original contributions are solicited on topics covered under broad areas such as (but not limited to):

- Device Physics and Modeling
- Nano-Structures Based Devices and Its Application
- Power Electronic Device, Circuits and Systems
- Photonics and Optoelectronics
- Embedded Devices (Logic + Memory- Memristor Based)
- Beyond CMOS: Nano Devices, Circuits & Hybrid Systems
- Analog / Mixed Signal Circuits & Systems
- Test and Verification
- Communications Circuits & Systems
- Neural Networks & Neuromorphic Circuits and Systems
- Visual Communications & Multimedia Signal Processing
- Internet of Things Circuits and Systems
- Cyber Physical System(CPS)
- EDA Methodologies
- Hardware Security
- 3D IC Design and Test
- Biomedical Circuits & Systems
- Digital Circuits & Systems
- Digital Signal Processing
- Nonlinear Systems & Circuit Theory
- Sensory Circuits & Systems
- Algorithms and Circuit implementations for AI and Machine Learning
- Photonics and Renewal Energy
- MEMS/MOEMS

Submission link: <https://easychair.org/conferences/?conf=isdcs2020>

The ISDCS'20 Program Committee invites original, unpublished paper submissions on the above topics. Paper submissions should be complete manuscripts, not exceeding six pages (including figures, tables, and bibliography) in a *standard IEEE two-column format*. The submission will be considered evidence that upon acceptance the author(s) will submit a final camera-ready version of the paper for inclusion in the proceedings, and will present the paper at the symposium. Each accepted contribution must have at least one full paid registration by the time the camera-ready paper is submitted for inclusion in the proceedings. ISDCS reserves the right to remove from IEEE Xplore papers not presented at the symposium.

#### Key Dates

<b>Submission deadline</b>	: 15 <sup>th</sup> December 2019
<b>Notification of acceptance</b>	: 31 <sup>st</sup> December 2019
<b>Camera ready manuscript</b>	: 15 <sup>th</sup> January 2020

Call for Papers



# ISDCS