

Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IEST), Shibpur Howrah-711103

Project on: "Visvesvaraya Ph.D. Scheme for Electronics and IT: Phase II"

[Funded by Digital India Corporation, MeitY, Government of India]

School of VLSI Technology
Indian Institute of Engineering Science & Technology (IEST), Shibpur Howrah-711103

Ref.: Advt. No. VL 1873, dated 13.09.2025

Interested candidates are requested to appear for walk-in interview on **10.10.2025 at 11.30 pm** in the Office of the Dean Research and Consultancy for the following post in the School of VLSI Technology Department, Indian Institute of Engineering Science & Technology (IEST), Shibpur, Howrah-711103

Name of the post: Post-Doctoral Fellow

No. of Post: - 1

Essential Qualification: Ph.D. with first class (in terms of grades) but not from the same Institution (host institution).

Age: The upper age limit for the fellowship is 40 years on the date of submission of application to the eligible participating institution (host institution).

Fellowship: ₹1,08,393/- per month (Consolidated)

Contingency Grant: ₹1,00,000/- per year (disbursed on a pro-rata basis, e.g. ₹25,000/quarter)

Experience / Software / Skillset:

Experience in Analog and Mixed Signal Design. Knowledge of VLSI EDA Tools, Verilog, RTL coding, and Functional Coding using C, Verilog-A.

Job Descriptions:

In addition to own research work, the awardees need to participate in activities under the Visvesvaraya PhD Scheme as assigned by PhD Cell such as:

- Evaluation of research work of PhD Candidates
- Mentoring of PhD candidates
- Participation in collaborative research under the scheme
- Participation in workshops and events
- Report significant achievements during the tenure

Duration: 1 year or until the completion of the project, subject to yearly evaluation of the candidate for the project execution.

Interested eligible candidates should mail soft copies of the application letter in plain paper, recent bio-data, marksheets, certificates research papers (if any), work experience certificate (if any), etc., in a single PDF file sent by email with "advertisement No." on the subject link to the below-mentioned e-mail ID. All documents should be self-attested. Physical documents will be verified at the time of joining. The selection will be cancelled if any discrepancies are found in the documents at the time of physical verification.


Venue of the interview: Office of the Dean (Research and Consultancy), IEST, Shibpur.

Note:

1. Soft copies of the application letter, bio-data, mark sheets and certificates should be sent through e-mail in advance by **08.10.2025 at 5.00 PM** to Prof. Hafizur Rahaman, Chief Investigator (E-mail id: hafizur@vlsi.iests.ac.in).
2. All applications must mention a valid e-mail id and phone number for communicating date of interview.
3. Short listing may be done before the interview.


Dean (R & C)

(W. Code DRC-008/25-26)

 **Prof. Pratik Dutta**
Dean, Research & Consultancy
Indian Institute of Engineering Science & Technology
Shibpur, Howrah-711 103, West Bengal, India