

**Project on: “Design and Development of WBG Device Based High current Converters for Industry Applications”**

[Sponsoring Authority: MeTy, Govt. of India]

**Department of Electrical Engineering,  
Indian Institute of Engineering Science & Technology (IEST), Shibpur  
Howrah-711 103**

Ref: Advt. No. EE 1812 published in the “Statesman (All Edition), dated 15.03.2023

[University Project Code: DRC/MEITY-NAMPET-III (WBG)/EE/MS/006/20-21]

Interested candidates are requested to appear for an online / hybrid / offline interview(s) for the following post in the Department of Electrical Engineering, **Indian Institute of Engineering Science & Technology (IEST), Shibpur, Howrah-711103.**

**Name of the post(s):** Project Associate.

**No of post(s) –** One (01)

**Essential Qualification(s):**

1. M.E. / M.Tech. or equivalent degree in Electrical Engineering from a recognized Indian University/Institute.
2. GATE qualification in Electrical Engineering or equivalent.
3. 1-2 years' experience (research/professional) in relevant field of Electrical engineering i.e., Power Electronics and Electrical Machine Drives.

**Desirable Qualification:**

1. Knowledge of simulation using FEM (like ANSYS MAXWELL) and power electronics simulation software.
2. Technical paper(s) in Electrical Engineering areas at least in reputed conferences.
3. First Class with CGPA 8.5 or above on a 10 point scale in Bachelor's and Master's Degree(s).
4. M.E. / M. Tech specialization in Power Electronics, Electrical Machines and Drives.
5. Hands on experience in Power Electronic circuit design, analysis, fabrication and testing.
6. Programming knowledge in any Digital Processor (such as FPGA / DSP).
7. Knowledge of open source software like SEQUEL, LT Spice etc.

**Fellowship:** Rs. 43,400/- Consolidated, as per Gol rules

**Age Limit:** Preferably below 30 years (Upper age limit is relaxable upto 5 years for SC/ST/OBC/Woman and physically handicapped candidates).

**Duration:** 12 months or till the termination of the project whichever is earlier, unless notified otherwise by the funding agency.

Interested eligible candidates should mail soft copies of the application letter in plain paper, recent bio-data, marksheets and certificates. All documents should be self-attested. Physical documents will be verified at the time of joining. The selection will be canceled if any discrepancies are found in the documents at the time of physical verification.

**Mode, Venue and Date of the interview:** Walk in interview at the Dept. of EE, IEST Shibpur. Information about the date and time of the interview will be sent to the eligible candidates only by e-mail.

**Note:**

1. Soft copies of the application letter, bio-data, mark sheets and certificates should be sent through e-mail in advance by 23/03/2023 to: Prof. Mainak Sengupta, Project Investigator (E-mail id: [msg.ee@faculty.iests.ac.in](mailto:msg.ee@faculty.iests.ac.in)).
2. All applications must mention a valid e-mail id and phone number for communicating date of interview.
3. Short listing may be done before the interview.

**(W. Code DRC-017/22-23)**

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